

100 →

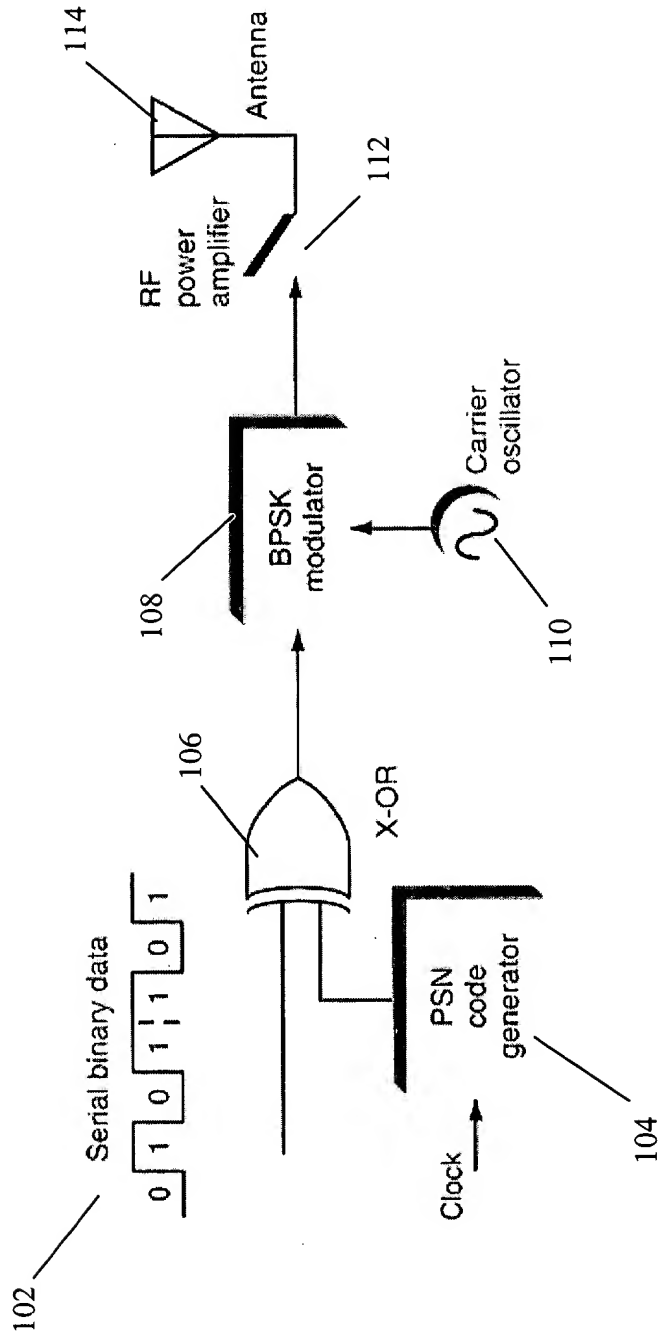


FIG. 1

(PRIOR ART)

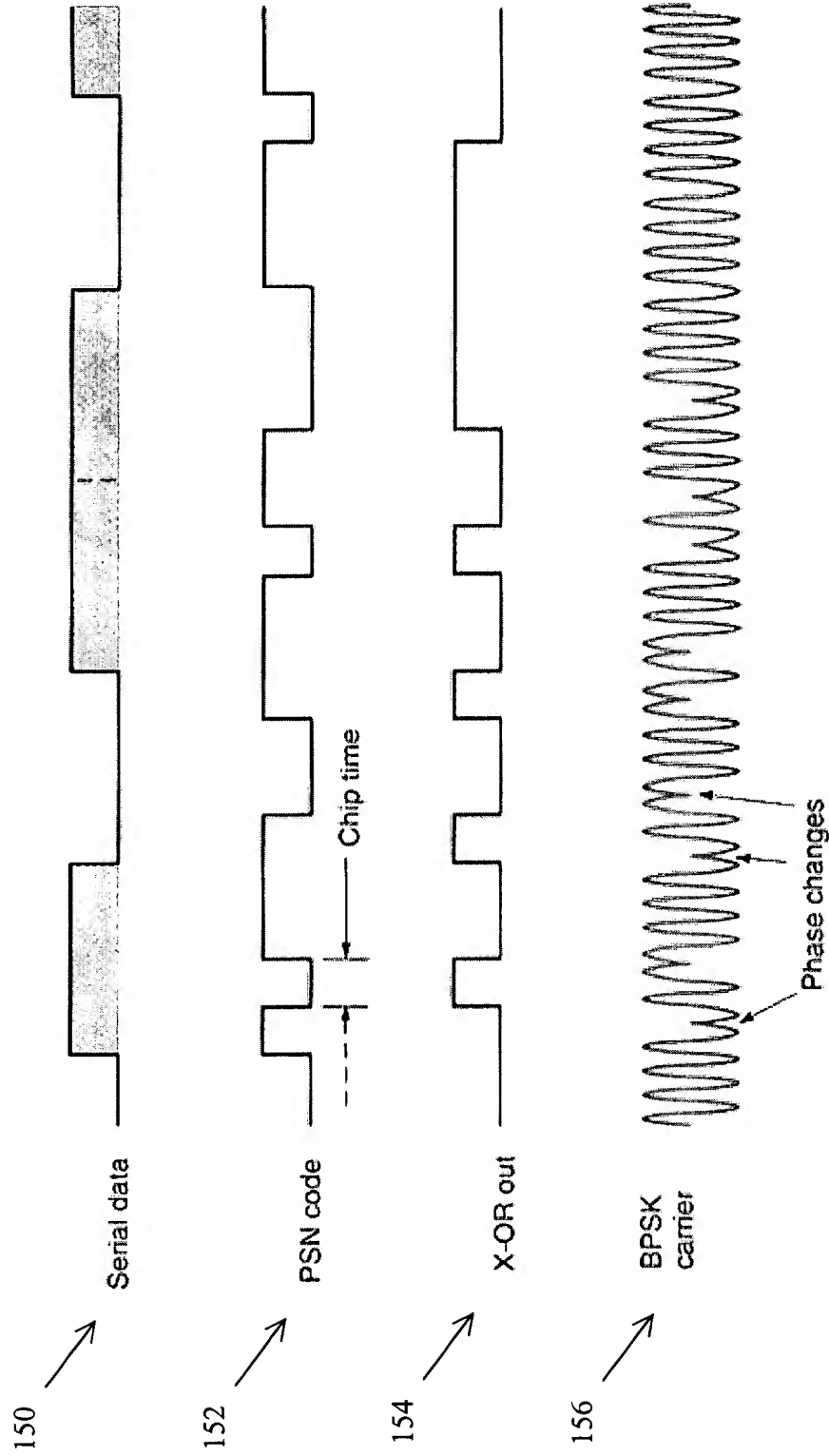


FIG. 2
(PRIOR ART)

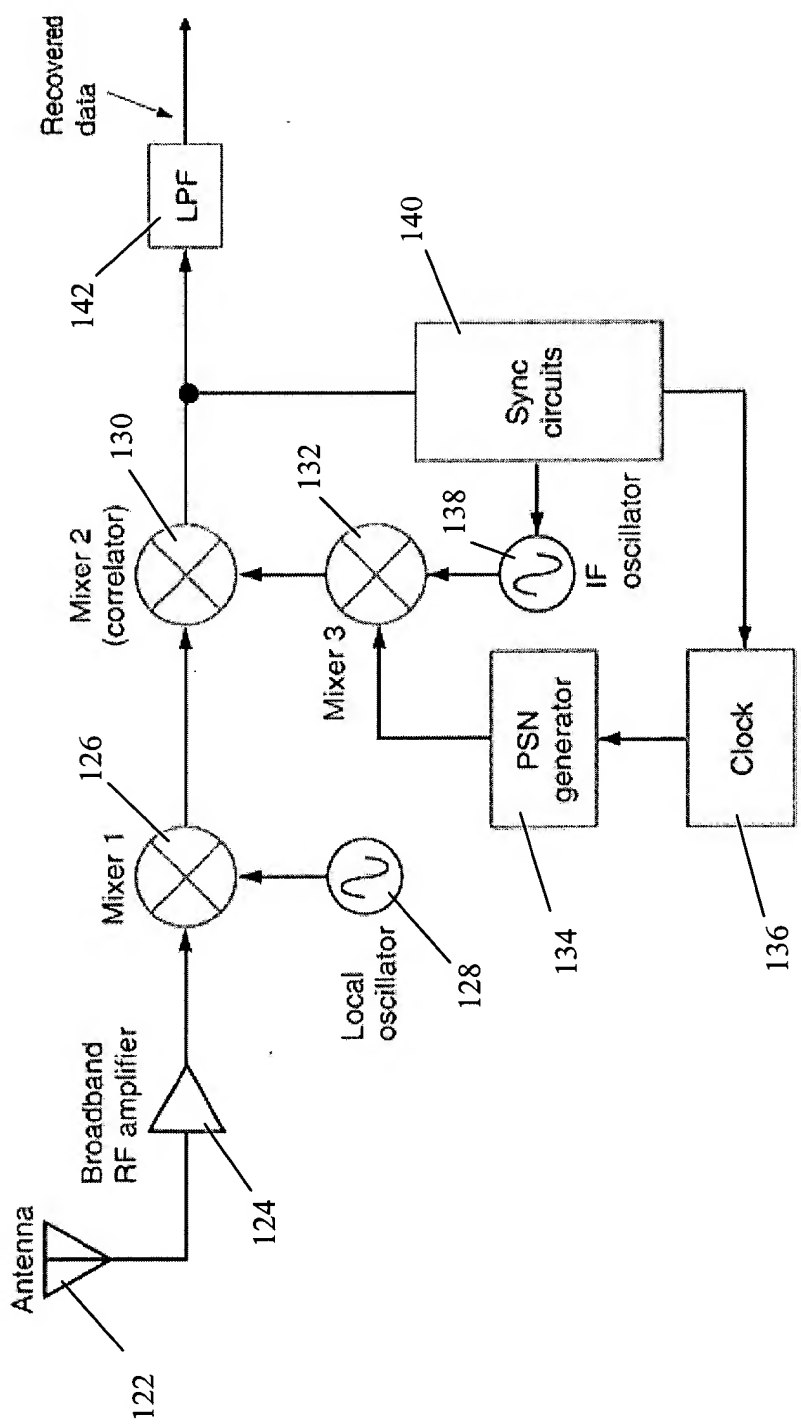


FIG. 3
(PRIOR ART)

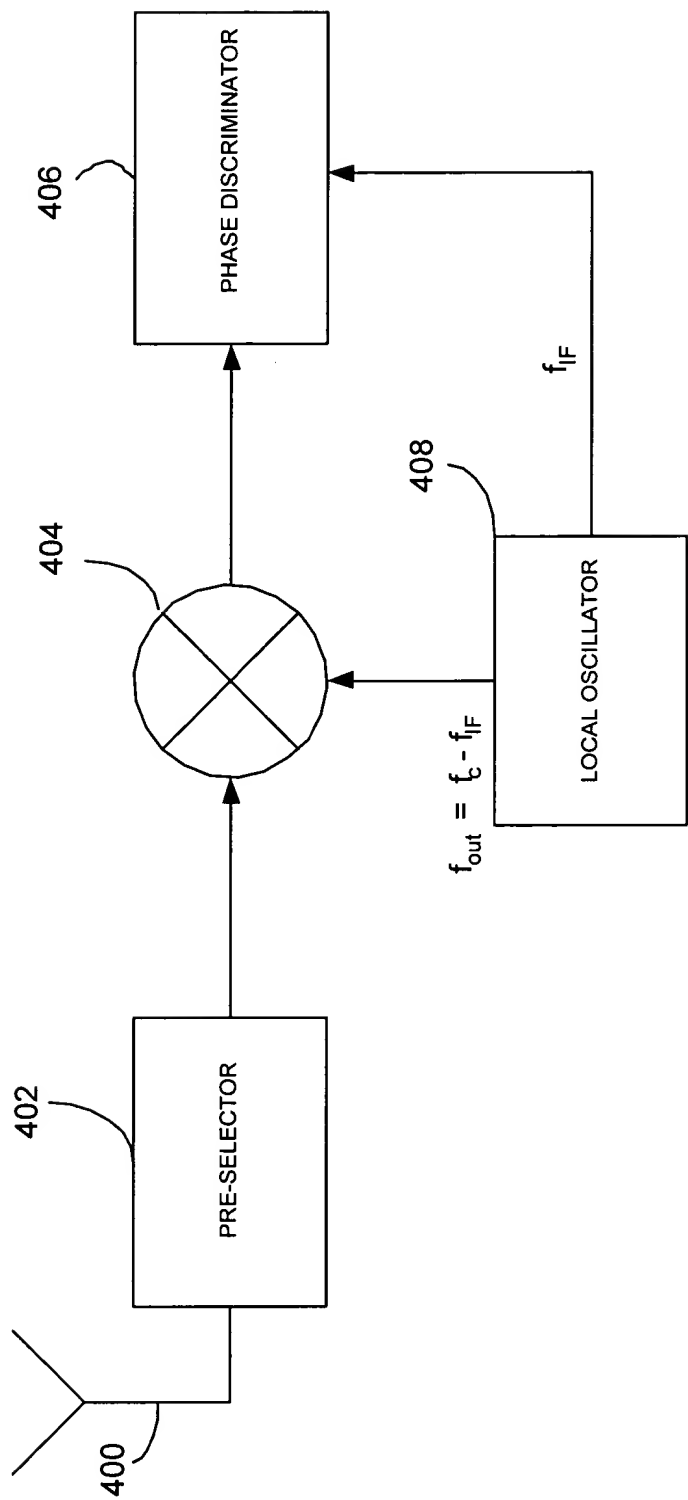


FIG. 4
(PRIOR ART)

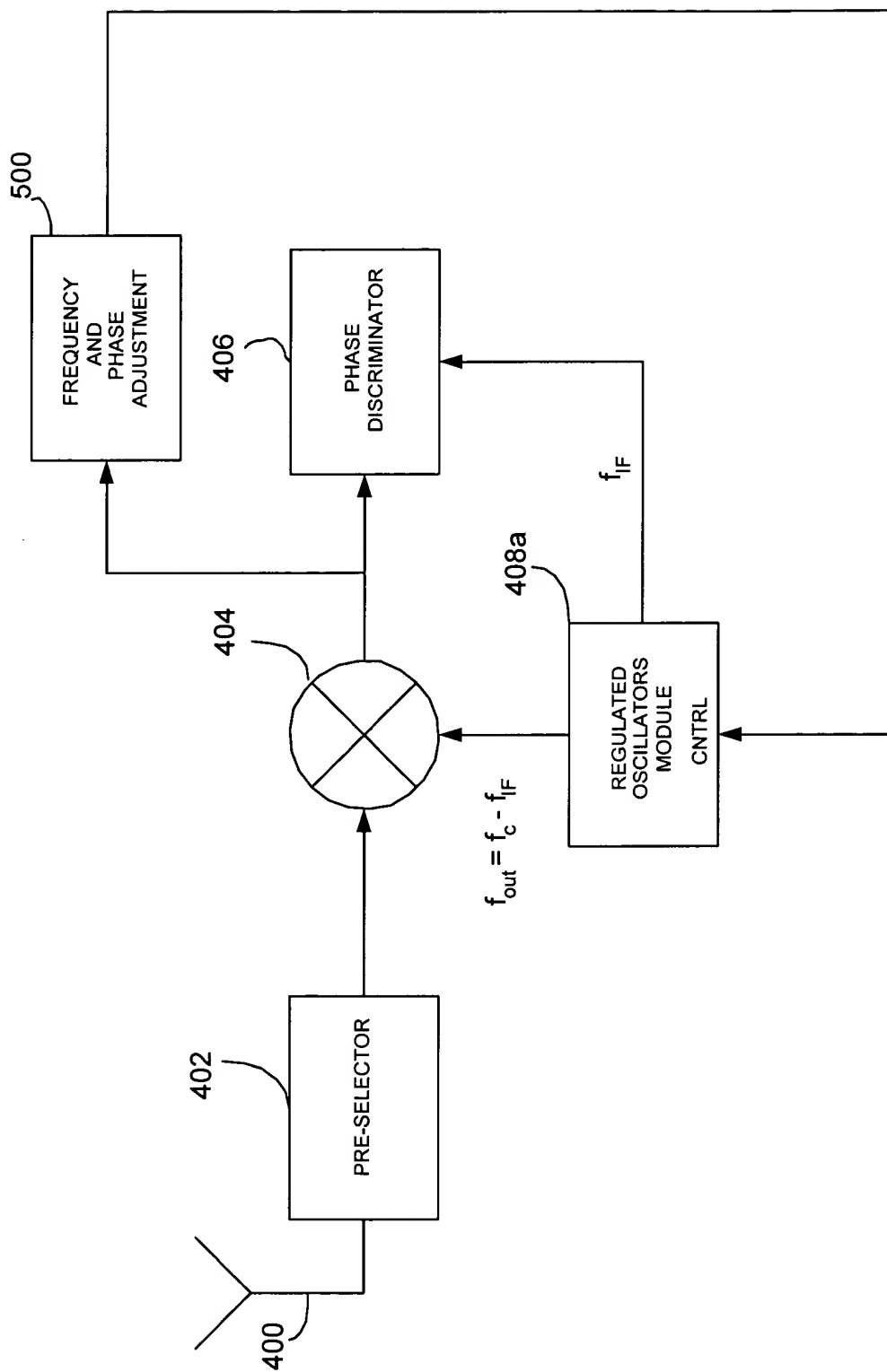


FIG. 5
(PRIOR ART)

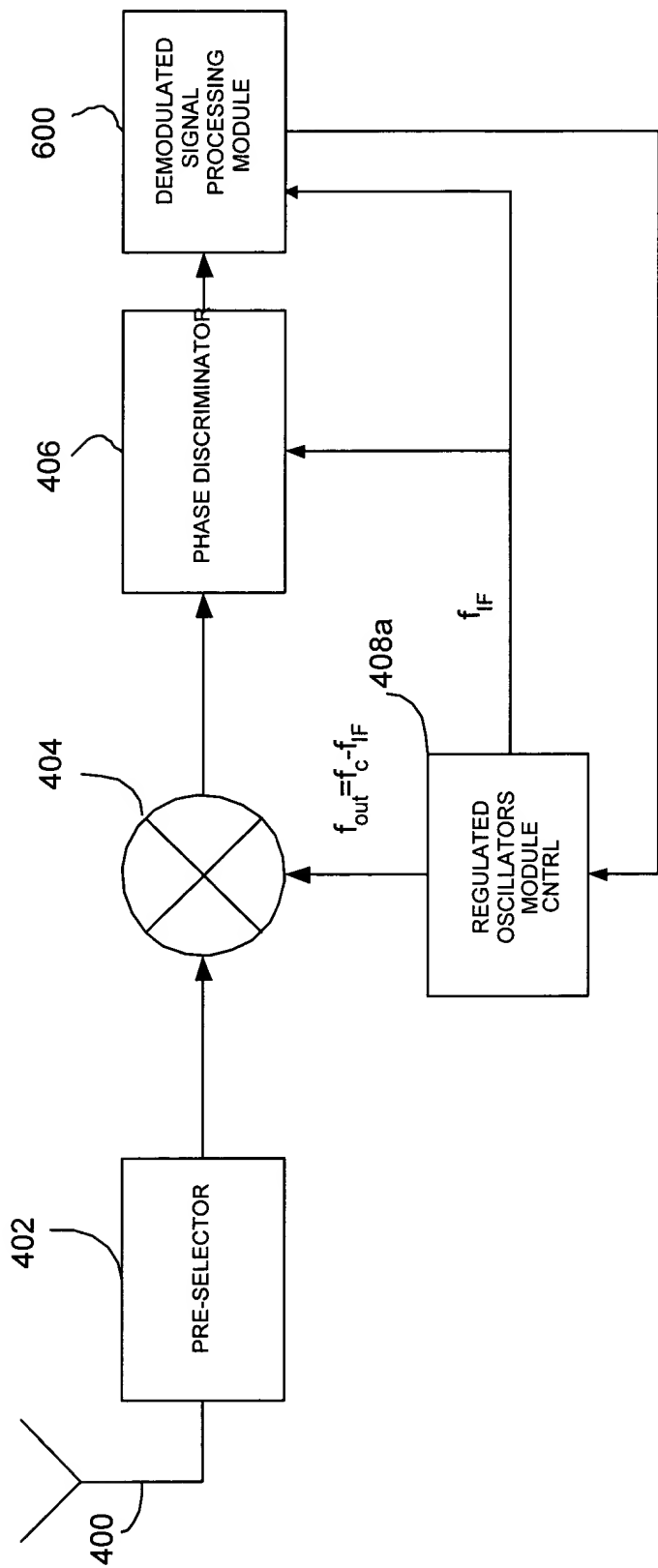


FIG. 6
(PRIOR ART)

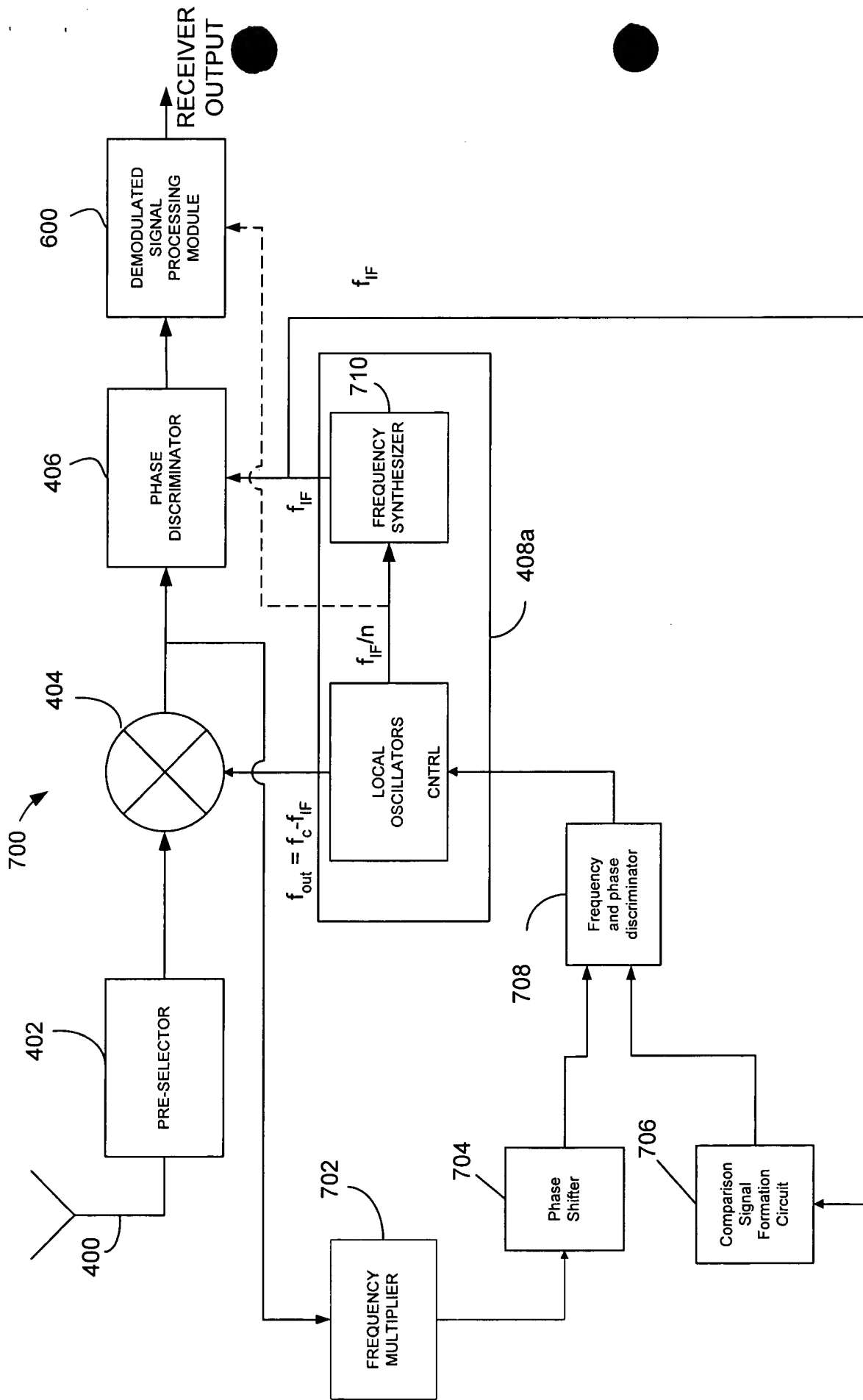


FIG. 7

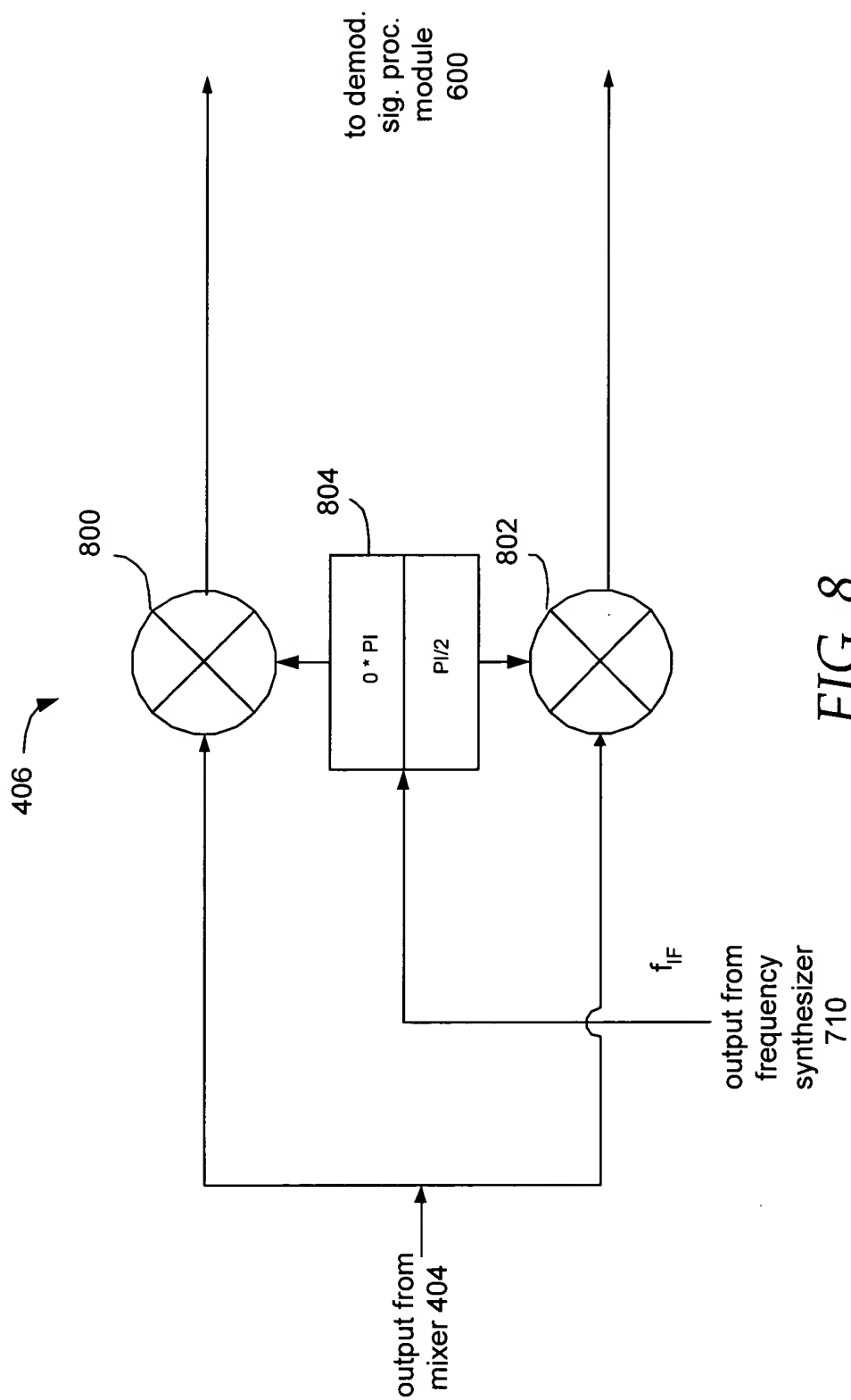


FIG. 8

FIG. 9 is a block diagram of a system 702 for processing a signal.

702

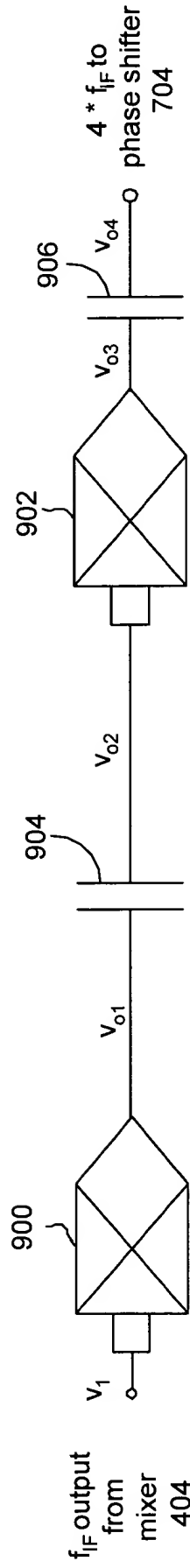


FIG. 9

FIG. 10 is a schematic diagram of a circuit for a frequency multiplier.

704

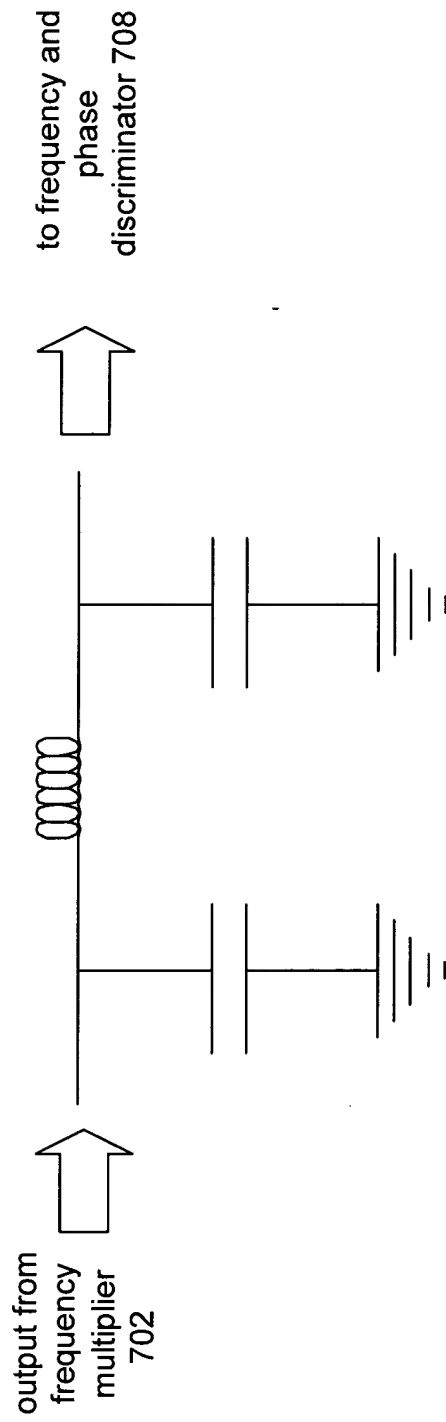


FIG. 10

FIG. 11 is a block diagram of a frequency synthesizer circuit.

706

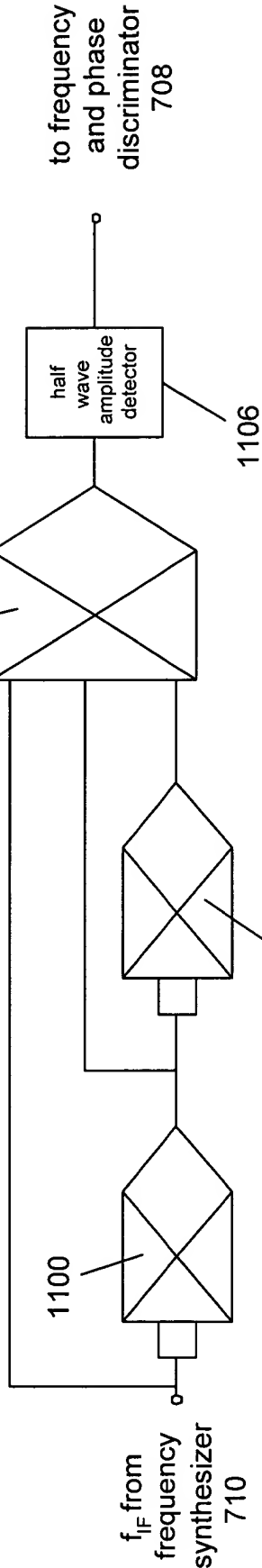


FIG. 11

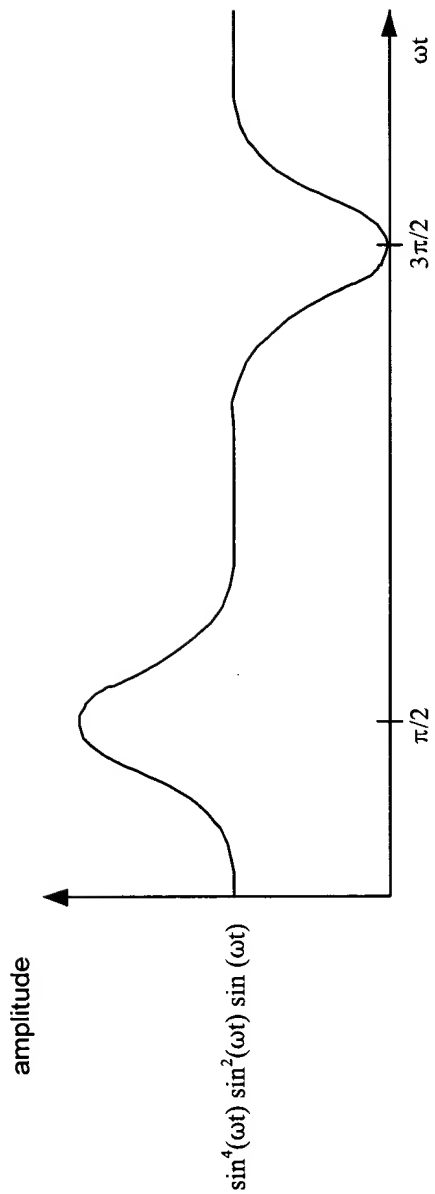


FIG. 12

FIG. 13

708

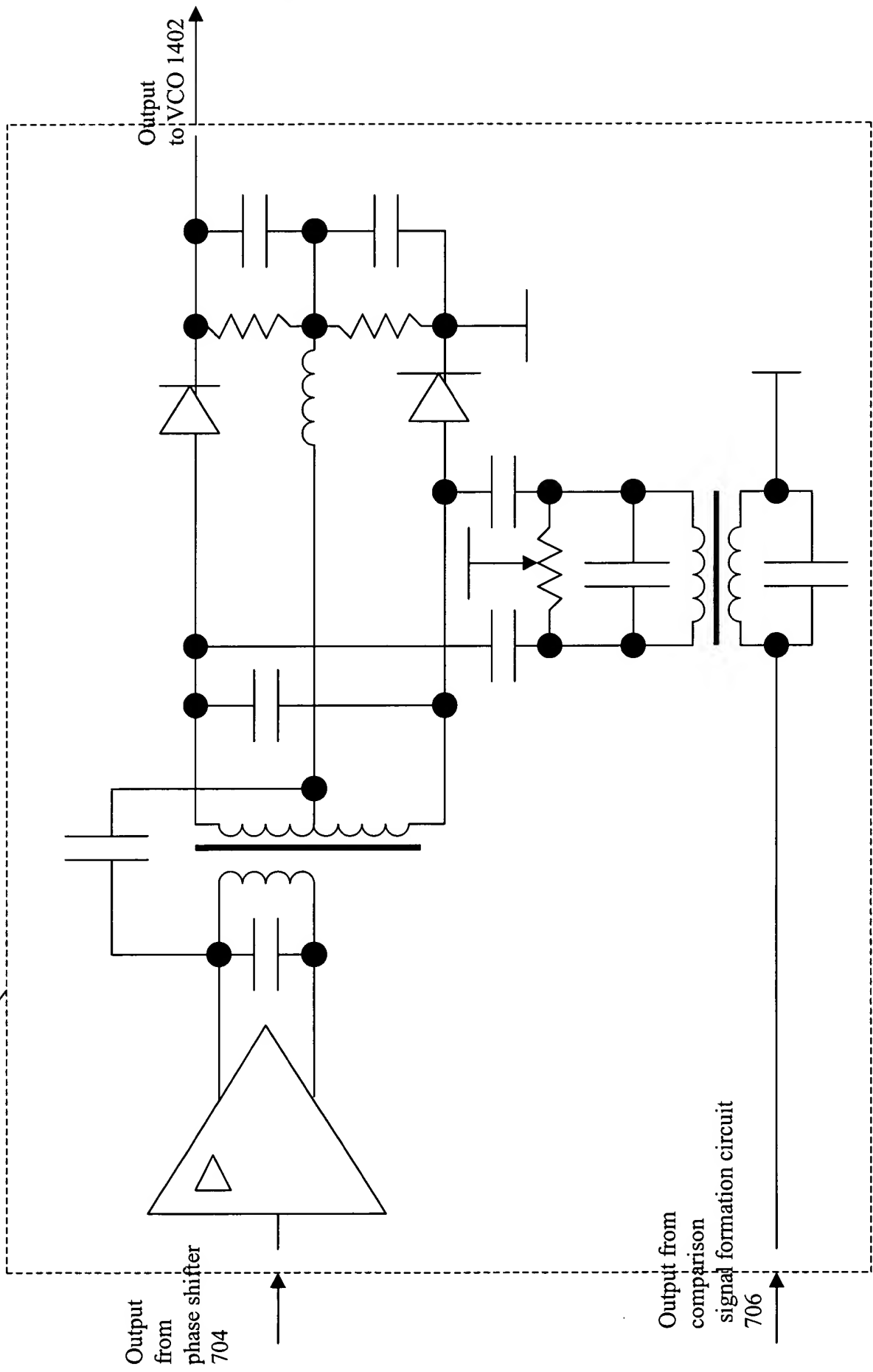


FIG. 13

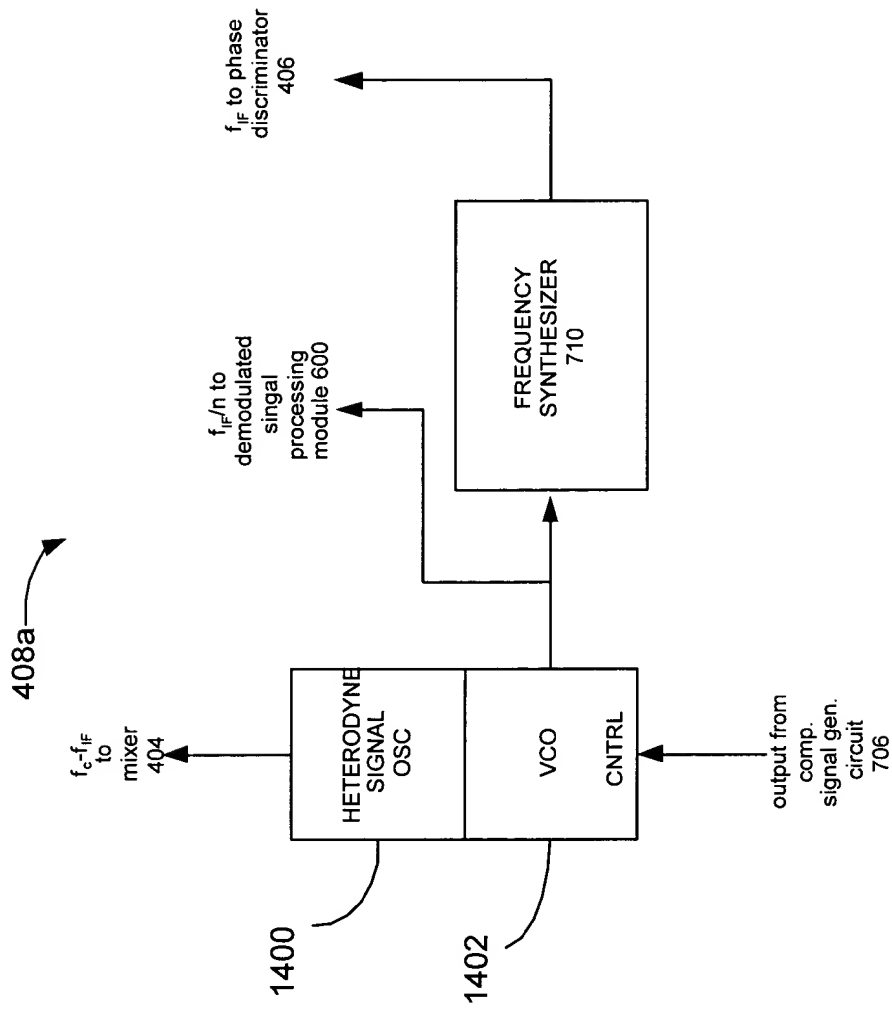


FIG. 14